

UNIVERSITY OF MADRAS
M.Sc. DEGREE PROGRAMME IN PHYSICS
 SYLLABUS WITH EFFECT FROM 2023-2024

Elective - List 4 – MICROPROCESSOR 8085 AND MICROCONTROLLER 8051	II YEAR – FOURTH SEMESTER
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Subject Code	Subject Name	Category	L	T	P	Credits	Inst. Hours	Marks
529E4A	MICROPROCESSOR 8085 AND MICROCONTROLLER 8051	ELECTIVE				3	4	75

Pre-Requisites
Knowledge of number systems and binary operations
Learning Objectives
<ul style="list-style-type: none"> ➤ To provide an understanding of the architecture and functioning of microprocessor 8085A and to the methods of interfacing I/O devices and memory to microprocessor ➤ To introduce 8085A programming and applications and the architecture and instruction sets of microcontroller 8051

UNITS	Course Details
UNIT I:8085 PROGRAMMING, PERIPHERAL DEVICES AND THEIR INTERFACING	Instruction set - Addressing modes - Programming techniques - Memory mapped I/O scheme- I/O mapped I/O scheme - Memory and I/O interfacing- Data transfer schemes - Interrupts of 8085 - Programmable peripheral interface (PPI) - Control group and control word- Programmable DMA controller - Programmable interrupt controller – Programmable communication interface - Programmable counter /interval timer.
UNIT II: 8085 INTERFACING APPLICATIONS	Seven segment display interface - Interfacing of Digital to Analog converter and Analog to Digital converter - Stepper motor interface - Measurement of electrical quantities –Voltage and current) Measurement of physical quantities (Temperature an strain).
UNIT III: 8051 MICROCONTROLLERHARDWARE	Introduction – Features of 8051 – 8051 Microcontroller Hardware: Pin-out 8051, Central Processing Unit (CPU), internal RAM, Internal ROM, Register set of 8051 – Memory organization of 8051 – Input/Output pins, Ports and Circuits – External data memory and program memory: External program memory, External data memory.

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UNIT IV: 8051 INSTRUCTION SET AND ASSEMBLY LANGUAGE PROGRAMMING	Addressing modes – Data moving (Data transfer) instructions: Instructions to Access external data memory, external ROM / program memory, PUSH and POP instructions, Data exchange instructions – Logical instructions: byte and bit level logical operations, Rotate and swap operations – Arithmetic instructions: Flags, Incrementing and decrementing, Addition, Subtraction, Multiplication and division, Decimal arithmetic – Jump and CALL instructions: Jump and Call program range, Jump, Call and subroutines – Programming.
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UNIT V: INTERRUPT PROGRAMMING AND INTERFACING TO EXTERNAL WORLD	8051 Interrupts – Interrupt vector table – Enabling and disabling an interrupt – Timer interrupts and programming – Programming external hardware interrupts – Serial communication interrupts and programming – Interrupt priority in the 8051 : Nested interrupts , Software triggering of interrupt. LED Interface Seven segment display interface- Interfacing of Digital to Analog converter and Analog to Digital converter - Stepper motor interface - Measurement of electrical quantities – Voltage and current) Measurement of physical quantities(Temperature an strain).
UNIT VI: PROFESSIONAL COMPONENTS	Expert Lectures, Online Seminars - Webinars on Industrial Interactions/Visits, Competitive Examinations, Employable and Communication Skill Enhancement, Social Accountability and Patriotism
TEXT BOOKS	<ol style="list-style-type: none"> 1. A. NagoorKani, Microprocessors & Microcontrollers, RBA Publications (2009). 2. A. P. Godse and D. A. Godse, Microprocessors, Technical Publications, Pune (2009). 3. Ramesh Gaonkar, Microprocessor Architecture, Programming and Applications with 8085, Penram International Publishing (2013). 4. B. Ram, Fundamentals of Microprocessors & Microcontrollers, DhanpatRai publications New Delhi (2016). 5. V. Vijayendran, 2005, Fundamentals of Microprocessor-8085”, 3rd Edition S.Visvanathan Pvt, Ltd.
REFERENCE BOOKS	<ol style="list-style-type: none"> 1. Douglas V. Hall, Microprocessors and Interfacing programming and Hardware, Tata Mc Graw Hill Publications (2008) 2. Muhammad Ali Mazidi, Janice GillispieMazidi, Rolin D. Mckinlay, The 8051 Microcontroller and Embedded Systems, Pearson Education (2008). 3. Barry B. Brey, 1995, The Intel Microprocessors 8086/8088, 80186, 80286, 80386 and 80486, 3rd Edition, Prentice- Hall of India, New Delhi. 4. J. Uffrenbeck, “The 8086/8088 Family-Design, Programming and

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	Interfacing, Software, Hardware and Applications”, Prentice-Hall of India, New Delhi. 5. W. A. Tribel, Avtar Singh, “The 8086/8088 Microprocessors: Programming, Interfacing, Software, Hardware and Applications”, Prentice-Hall of India, New Delhi.
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WEB SOURCES	<ol style="list-style-type: none"> https://www.tutorialspoint.com/microprocessor/microprocessor_8085_architecture.html http://www.electronicengineering.nbcafe.in/peripheral-mapped-io-interfacing/ https://www.geeksforgeeks.org/programmable-peripheral-interface-8255/ http://www.circuitstoday.com/8051-microcontroller https://www.elprocus.com/8051-assembly-language-programming/
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COURSE OUTCOMES:

At the end of the course, the student will be able to:

CO1	Gain knowledge of architecture and working of 8085 microprocessor.	K1
CO2	Get knowledge of architecture and working of 8051 Microcontroller.	K1
CO3	Be able to write simple assembly language programs for 8085A microprocessor.	K2, K3
CO4	Able to write simple assembly language programs for 8051 Microcontroller.	K3, K4
CO5	Understand the different applications of microprocessor and microcontroller.	K3, K5
K1 - Remember; K2 – Understand; K3 - Apply; K4 - Analyze; K5 - Evaluate;		

MAPPING WITH PROGRAM OUTCOMES:

Map course outcomes (CO) for each course with program outcomes (PO) and program specific outcomes (PSO) in the 3-point scale of STRONG (3), MEDIUM (2) and LOW (1).

	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10
CO1	2	3	3	3	3	1	1	1	1	1
CO2	2	1	1	1	1	1	1	1	1	1
CO3	3	3	3	3	3	1	1	1	1	1
CO4	3	3	3	3	3	1	1	1	1	1
CO5	3	3	3	3	3	1	1	1	1	1

	PSO1	PSO2	PSO3	PSO4	PSO5	PSO6	PSO7	PSO8	PSO9	PSO10
CO1	2	3	3	3	3	1	1	1	1	1
CO2	2	1	1	1	1	1	1	1	1	1
CO3	3	3	3	3	3	1	1	1	1	1
CO4	3	3	3	3	3	1	1	1	1	1
CO5	3	3	3	3	3	1	1	1	1	1